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PTO/SB/08A (08-00)

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| Substitute for form 1449A/PTO<br><b>INFORMATION DISCLOSURE<br/>STATEMENT BY APPLICANT</b><br>(use as many sheets as necessary) |        | <b>Complete if Known</b> |                 |
|  |        | Application Number       | 09/844,668      |
|  |        | Filing Date              | April 30, 2001  |
|  |        | First Named Inventor     | THEKKATH        |
|  |        | Group Art Unit           | 2192            |
|  |        | Examiner Name            | Pham, Chrystine |
| Sheet  | 1 of 1 | Attorney Docket Number   | MTEC-004/00US   |

| OTHER – NON PATENT LITERATURE DOCUMENTS |                       |   |                |
|---|-----------------------|---|----------------|
| Examiner Initials*                      | Cite No. <sup>1</sup> | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. | T <sup>2</sup> |
| CP                                      | D1.                   | F. Chow et al., "Engineering a RISC Compiler System," <i>IEEE COMP-CON</i> , March 1986, pp. 132-137.   |                |
| CP                                      | D2.                   | M.D. Smith, "Tracing with Pixie," <i>Technical Report CSL-TR-91-497</i> , Stanford University, Computer Systems Laboratory, November 1991, pp. 1-29.  |                |
| CP                                      | D3.                   | ATOM Reference Manual, <i>Digital Equipment Corporation</i> , Massachusetts, December 1993, pp. 1-32.   |                |
| CP                                      | D4.                   | A. Srivastaba et al., "ATOM: A System for Building Customized Program Analysis Tools," <i>WRL, Research Report 94/2</i> , Digital Equipment Corporation, Massachusetts, March 1994, pp. 1-23.   |                |
| CP                                      | D5.                   | ATOM User Manual, Digital Equipment Corporation, March 1994, pp. 1-28.  |                |
| CP                                      | D6.                   | B. Cmelik et al., "Shade: A Fast Instruction-Set Simulator for Execution Profiling," <i>Proceedings of the 1994 ACM SIGMETRICS Conference</i> , SIGMETRICS, California, May 1994, pp. 128-137.  |                |
| CP                                      | D7.                   | MIPS Technologies; "MIPS64 5K™ Processor Core Integrator's Guide;" [Document No. MD00056]; <i>MIPS Technologies, Inc.</i> ; Rev. 1.2; August 11, 2000; pp. I-II, 1-82.  |                |
| CP                                      | D8.                   | MIPS Technologies; "MIPS64™ 5K™ Processor Core Family Integrator's Guide;" [Document No. MD00106]; <i>©1999-2001 MIPS Technologies, Inc.</i> ; Rev. 02.00; January 15, 2001; pp. I-VIII, 1-75.  |                |
| CP                                      | D9.                   | MIPS Technologies; "Core Processor Interface Specification;" [Document No. MD00068]; <i>MIPS Technologies, Inc.</i> ; Rev. 1.11; March 30, 2001; pp. 1-26.  |                |
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| Examiner Signature | /Chrystine Pham/ | Date Considered<br>(08/16/2006) |  |
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<sup>1</sup> Unique citation designation number.

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|  |        | First Named Inventor     | THEKKATH        |
|  |        | Group Art Unit           | 2122            |
|  |        | Examiner Name            | Pham, Chrystine |
| Sheet  | 1 of 1 | Attorney Docket Number   | MTEC-004/00US   |

| FOREIGN PATENT DOCUMENTS |                          |                         |                     |                                      |  |  |                |
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| Examiner<br>Initials*    | Cite<br>No. <sup>1</sup> | Foreign Patent Document |                     |                                      | Name of Patentee or Applicant of Cited<br>Document | Date of Publication<br>of Cited Document<br>MM-DD-YYYY | T <sup>4</sup> |
|                          |                          | Office <sup>1</sup>     | Number <sup>2</sup> | Kind Code <sup>3</sup><br>(if known) |  |  |                |
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| CP                                      | D1                    | Susan J. EGGERS, et al., "Techniques for Efficient Inline Tracing on a Shared-Memory Multiprocessor," University of Washington, 1990 ACM, pp. 37-47.  |                |
| CP                                      | D2                    | Richard A. UHLIG, et al., "Trace-Driven Memory Simulation: A Survey," Intel Microcomputer Research Lab; University of Michigan, ACM Computing Surveys, Vol. 29, No. 2, June 1997, pp. 128-170.  |                |
| CP                                      | D3                    | Eric ROTENBERG, et al., "Trace Processors," University of Wisconsin, 1997 IEEE Service Center, 12 pp.   |                |
| CP                                      | D4                    | E.N. ELNOZAHY, "Address Trace Compression Through Loop Detection and Reduction," ©1999 ACM 1-58113-083, pp. 214, 215.   |                |
| CP                                      | D5                    | Darren JONES, "Opal Coprocessor Interface," MIP Proprietary/Confidential, June 4, 1999, pp. 1-18.   |                |
| CP                                      | D6                    | Darren JONES, MIPSS64™ SKC™ Processor Cores User's Manual, Rev. 1.0, July 4, 1999, pp. 6-1 to 6-26.   |                |
| CP                                      | D7                    | MIPSS64™ SKC™ Processor Cores User's Manual, Rev. 1.0.1., 1999, pp. 247-276.  |                |
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|                    | /Chrystine Pham/ | (08/16/2006)    |  |

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